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NBA Accredited (CSE, ECE, EEE, MECH, CIVIL)

## EXPERT LECTURE ON "GATE PULSE GENERATION FOR THE POWER CONVERTERS USING DSPACE 1104"

Event No	EE001			
Organizing Department	Electrical and Electronics Engineering			
Date	06/04/2024			
Time	11:00 AM to 01:00 PM			
Event Type	Expert Talk			
Event Level	Dept. Level			
Venue	Power Electronics Laboratory			
Meeting Medium				
Meeting Link	https://meet.google.com/vip-heky-huh			
Total Participants	48			
Faculty - Internal	8			
Students - Internal	40			

#### Related SDG



#### **Resource Persons**

SI	Туре	Name	Designation	Company	Email	Phone
1	Resource Person	Dr G Arunkumar	Associate Professor Senior	Vellore Institute of Technology, Vellore	g.arunkumar@vit.ac.in	xxxxxxxxx

#### Involved Staffs

SI	Name	Role
1	Revathi R	Coordinator

### Outcome

Learnt how to generate PWM signals for controllers using DS1104

## **Event Summary**

The Department of Electrical and Electronics Engineering organized expert lecture on "Gate Pulse Generation for the Power Converters using dSPACE 1104" for the students, teaching and non teaching staffs on 06/04/2024. A heart-warming welcome was delivered by Prof.R.Revathi, AP (Sr.G)/EEE and Dr.K.Mohana Sundaram, Prof & Head/EEE addressed the gathering and shared the importance and opportunities of dSPACE software in power electronics field. Dr. G. Arunkumar, Associate Professor Senior, Department of Energy and Power Electronics, School of Electrical Engineering (SELECT), Vellore Institute of Technology, Vellore has been invited as a resource person to deliver the lecture on gate pulse generation for power converters using ds1104. He has shared his personal experience about how to record pulses using DS1104. He shared the data sheet and software tools available in dSPACE software. He also delivered the applications of dSPACE software in power grid, Micro lab box and its features, Control desk and RTI blockset and Rapid control prototyping. During the demo session, he demonstrated how to generate PWM signals for controllers and how to interface the hardware using ADC & DAC channels. The session was an eye opening for all the participants. The session was ended with vote of thanks.

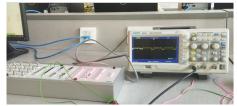




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